

Amendments to the Specification

Paragraph bridging pages 12-13:

Fig. 1 is a block diagram of an electronic cash register system according to the present invention. The electronic cash register system includes a plurality of electronic cash registers 23 and a kitchen video controller 20 having a display 21 and monitor switch 22. The kitchen video controller 20 includes a CPU 1, a program memory 2, a constant data memory 3, a unit interval selling memory 4, a stock memory 5, a pending memory 6, an interval memory 7, a display memory 8, a display control circuit 9, a monitor switch interface circuit 10, a communication circuit 11 for communicating with the electronic cash registers 23, a clock circuit 12 for generating a time signal and a date signal.

Page 14, second paragraph:

Fig. 2A is an illustration showing data format of the interval memory 7. The interval memory 7 stores a plurality of sets of unit interval data, that is, five-minute prior unit interval data, ten-minutes prior unit interval data, and fifteen-minute prior unit interval data in order of time. The five-minute prior unit interval data includes the number of articles which have been sold preceding, or just before five minutes. The ten-minute prior unit interval data includes the number of articles which was sold for the second prior unit interval, that is, sold from ten to five minutes before. The ten-minute prior unit interval data includes the number of articles which was sold for the second prior unit interval, that is, sold from ten to five minutes before.